

DC COMPONENTS CO., LTD.

RECTIFIER SPECIALISTS

FR1AFL THRU FR1MFL

TECHNICAL SPECIFICATIONS OF FAST RECOVERY RECTIFIER VOLTAGE RANGE - 50 to 1000 Volts CURRENT - 1.0 Ampere

FEATURES

- * Ideal for surface mounted applications
- * Low leakage current
- * Glass passivated junction
- * High efficiency
- * Fast reverse recovery time

MECHANICAL DATA

* Case: Molded plastic

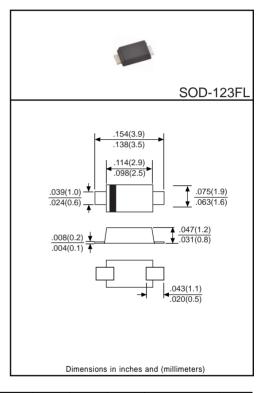
* Epoxy: UL 94V-0 rate flame retardant *Terminals: Solder plated, solderable per MIL-STD-750, Method 2026

* Polarity: As marked * Mounting position: Any * Weight: 0.02 gram

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%.



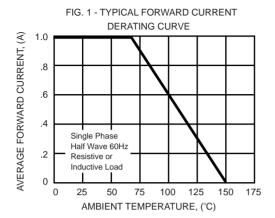
		SYMBOL	FR1AFL	FR1BFL	FR1DFL	FR1GFL	FR1JFL	FR1KFL	FR1MFL	UNITS
Maximum Recurrent Peak Reverse Voltage		VRRM	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage		VRMS	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage		VDC	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current at TA = 65°C		lo	1.0							Amps
Peak Forward Surge Current IFM (surge): 8.3 ms single half sine-wave superimposed on rated load (JEDEC method)		IFSM	25							Amps
Maximum Forward Voltage at 1.0A DC		VF	1.3							Volts
Maximum DC Reverse Current at	@TA = 25°C		5.0							μAmps
Rated DC Blocking Voltage	@T _A = 125°C	IR	100							μππιβ
Maximum Reverse Recovery Time (Note 1)		trr		150			250	500		nSec
Typical Thermal Resistance (Note 2)		RθJA	120							°C/W
Typical Junction Capacitance (Note 3)		Cı	15							pF
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150							٥C

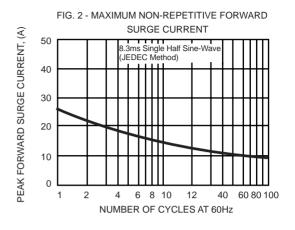
NOTES: 1. Test Conditions: IF=0.5A, IR=1.0A, IRR=0.25A.

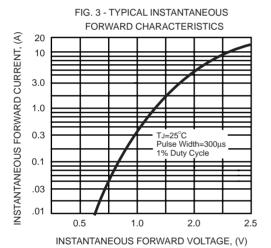
- 2. P.C.B. mounted with 0.2x0.2 in² (5x5mm²) copper pads to each terminal.
- 3. Measured at 1.0 MHz and applied reverse voltage of 4.0 VDC.

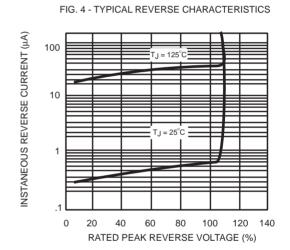
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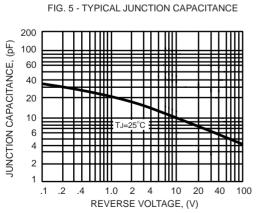
RATING AND CHARACTERISTIC CURVES (FR1AFL THRU FR1MFL)











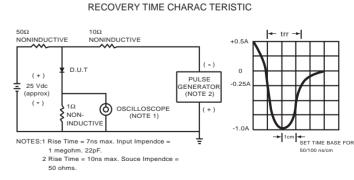


FIG. 6 - TEST CIRCUIT DIAGRAM AND REVERSE

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