



DC COMPONENTS CO., LTD.
DISCRETE SEMICONDUCTORS

2N6395
THRU
2N6398

TECHNICAL SPECIFICATIONS OF SILICON CONTROLLED RECTIFIERS
VOLTAGE RANGE - 100 to 600 Volts CURRENT - 12 Amperes

Description

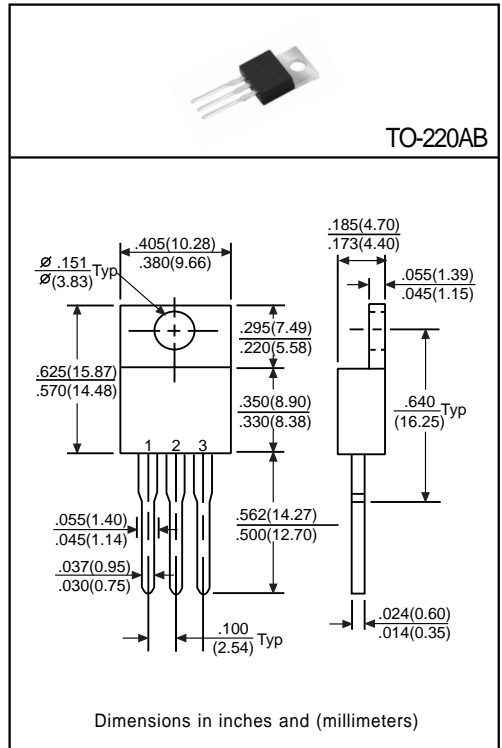
- * Driven directly with IC and MOS device
- * Feature proprietary, void-free glass passivated chips
- * Available in voltage ratings from 100 to 600 volts
- * Non-sensitive gate trigger current
- * Designed for high volume, line-powered control application in relay lamp drivers, small motor controls, gate drivers for large thyristors

Pinning

1 = Cathode, 2 = Anode, 3 = Gate

Absolute Maximum Ratings (T_A=25°C)

Characteristic	Symbol	Rating	Unit	
Peak Repetitive Off-State Voltage and Reverse Voltage	2N6395 2N6396 2N6397 2N6398	V _{DRM} , V _{VRRM}	100 200 400 600	V
On-State RMS Current (T _A =57°C, 180° Conduction Angles)	I _{T(RMS)}	12	A	
Peak Non-repetitive Surge Current (1/2 Cycle, Sine Wave 60Hz)	I _{TSM}	100	A	
Forward Peak Gate Current	I _{GM}	2.0	A	
Forward Peak Gate Power Dissipation	P _{GM}	20	W	
Forward Average Gate Power Dissipation	P _{G(AV)}	0.5	W	
Operating Junction Temperature	T _J	-40 to +110	°C	
Storage Temperature	T _{STG}	-40 to +150	°C	



Electrical Characteristics

(Ratings at 25°C ambient temperature unless otherwise specified)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Peak Repetitive Forward or Reverse Off-State Blocking Current	T _J =25°C	-	-	10	μA	V _{AK} =Rated V _{DRM} or V _{VRRM} R _{GK} =1KΩ
	T _J =110°C	-	-	2000		
Peak Forward On-State Voltage	V _{TM}	-	-	2.2	V	I _{TM} =12A Peak
Continuous DC Gate Trigger Current	I _{GT}	-	-	20	mA	V _{AK} =7V DC, R _L =100Ω
Continuous DC Gate Trigger Voltage	V _{GT}	-	-	2.0	V	V _{AK} =7V DC, R _L =100Ω
DC Holding Current	I _H	-	-	50	mA	R _{GK} =1KΩ
Critical Rate-of-Rise of Off-State Voltage	dv/dt	-	50	-	V/μS	R _{GK} =1KΩ
Gate Controlled Turn-on Time(t _d +t _{tr})	T _{gt}	-	2.2	-	μsec	I _{GT} =10mA
Thermal Resistance, Junction to Case	R _{θJC}	-	2.0	-	°C/W	-